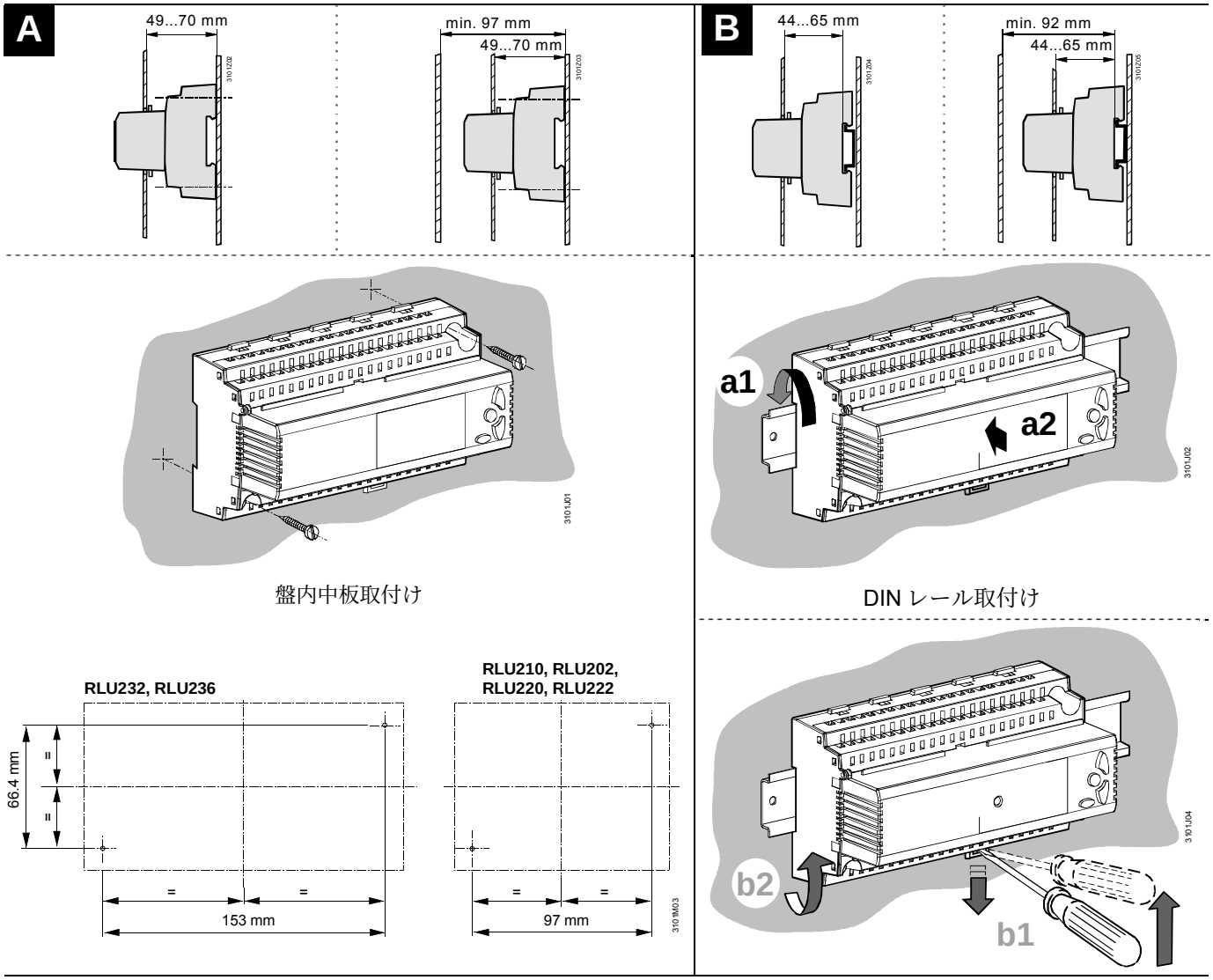
取扱説明書

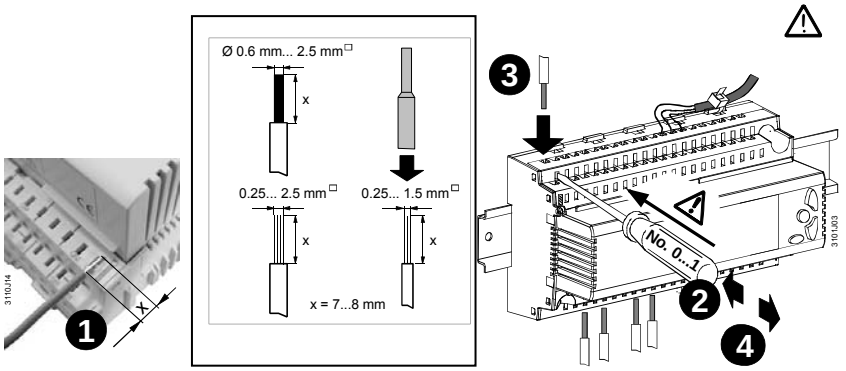
RLU2...

＜取付け＞



盤内中板取付け

DIN レール取付け



＜配線＞

現場の配線図に基づき、以下に示す①～④の手順で正しく配線を接続して下さい。

接続端子は、スプリットケージ式でワンタッチで接続できる様になっております。

①ケーブルの絶縁被覆を剥いて芯線を 7~8mm 程度出す。

②マイナスドライバーをスプリットに挿入し押したままで、

③ケーブル芯線を端子に挿入し、
④ドライバーを抜くとスプリングの力で自動的に芯線が端子に固定される。

⚠ **＜配線上の注意＞**

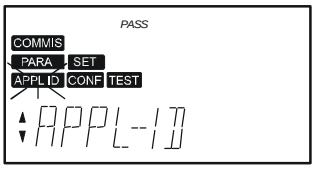
- **電気配線は、有資格者が行い、関連法規、各種規定等に基づいて施工して下さい。**
- **各プラント配線図（計装図）を参照し、正しく接続して下さい。**
- **調節器の端子にダブル配線は出来ません。各端子には必ず配線１本のみを接続して下さい。**
- **調節器の端子台にはダブル端子マーク( )がついて、同じ端子番号が付されたものが有ります。これらは内部で渡り配線となってる端子を示します。**

調整（コミッショニング）

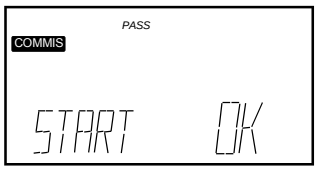
スタートアップ

1. 電源 ON
   調整中は、外部出力をしません。
   この間、外部のセーフティー機能も働かないので注意して下さい！

   最初、電源 ON で立上ると、コミッショニングメニュー COMMIS に入り、サブメニューのパラメーター PARA が点滅します。.
2. まず、(+/–)ボタンでアプリケーション APPL ID を選択、点滅させます。

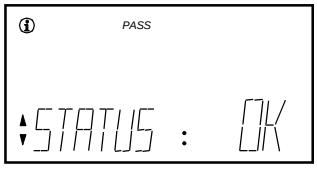


3. 1 回 OK を押すと、上の画面が現れるので、さらにもう 1 回 OK を押すと、基本アプリケーション番号（例：A01）が点滅します。
4. (+/–)ボタンで必要なアプリケーションを選び、OK ボタンで確定します。
5. 必要に応じコンフィギュレーション CONF メニューに入り、入出力の構成を設定、または変更する事が可能です。基本アプリケーション通りの構成で有れば通常は変更不要です。
6. 同様に PARA メニューで各種の制御パラメーターを設定、変更する事が出来ます。基本アプリケーション通りの構成で有れば通常は変更不要です。
7. 入出力のワイヤリング試験のため TEST メニューで確認する事をお勧めします。各入出力毎に値が表示されます。
   注：確認中は、セーフティー機能は働きません！
8. COMMIS メニューを終了するには、ESC ボタンを押し、次の画面を表示させます：



9. OK ボタンを押します。
   この時点で、制御動作を開始、全ての検出器入力をチェックし、将来検出器にエラーが有る様な場合、その検出器の表示窓にエラー表示を行います。
   画面は、以下を表示します。

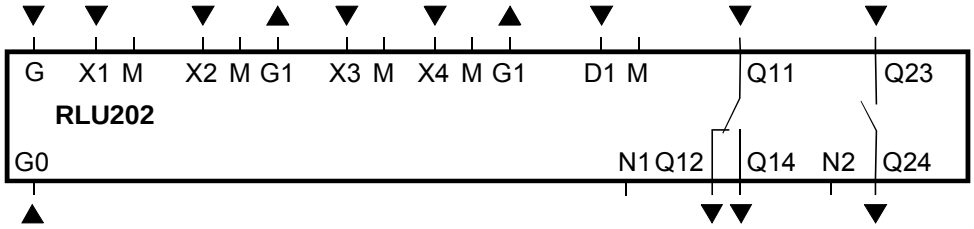
10. 各設定値を入力するために (+/–) ボタンで SET メニューを点滅させてから、OK を押します。各設定値を変更する場合、(+/–) ボタンで設定項目を探し、OK で点滅させてから (+/–) ボタンで値を変更し OK で確定します。上の画面から、必要に応じて PARA パラメニューに入り、パラメーターの確認、設定変更も可能です。
11. 通常運転画面に戻るため ESC ボタンを 2 回押します。
    調節器が正常運転中で有れば以下の画面が表示されます。

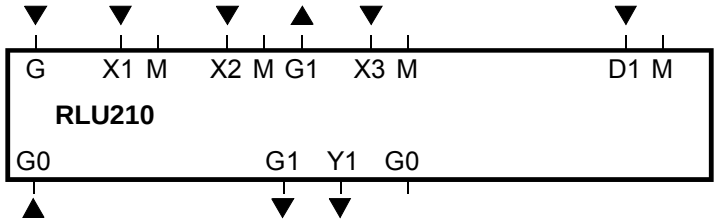


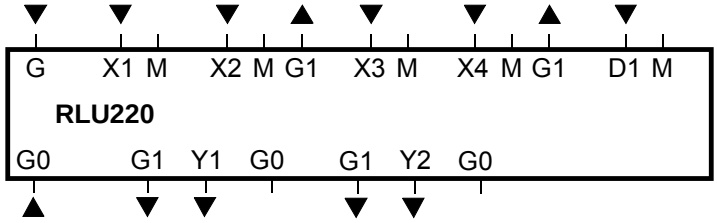
注意：
COMMIS メニューはアクセスレベルがパスワードレベルでないと操作出来ません（password = 2）

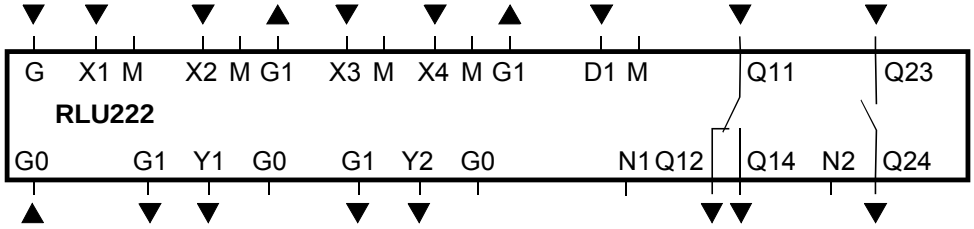
- アクセスレベルは以下の 3 つです。
  – ユーザーレベル（インフォレベル） ⓘ
  – サービスレベル *SERVICE*
  – パスワードレベル *PASS*
- アクセスレベルは、ESC と OK ボタンを同時に押して変更可能です。
  アクセスレベルをパスワードレベル *PASS*（password = 2）にすれば、いつでも、左の 2.の画面に入ることが出来ます。
- SET メニューでは、登録したアプリケーションに必要なすべての設定値が表示されます。

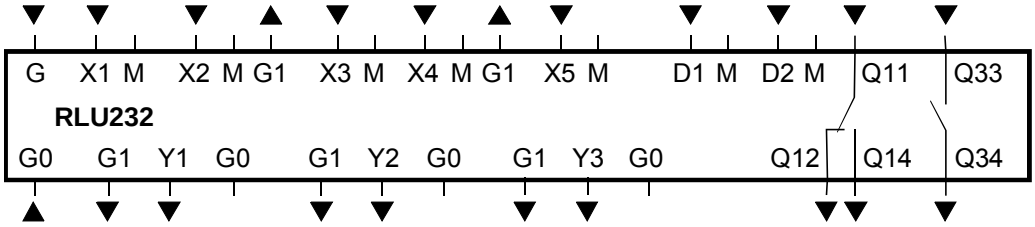
＜接続端子＞

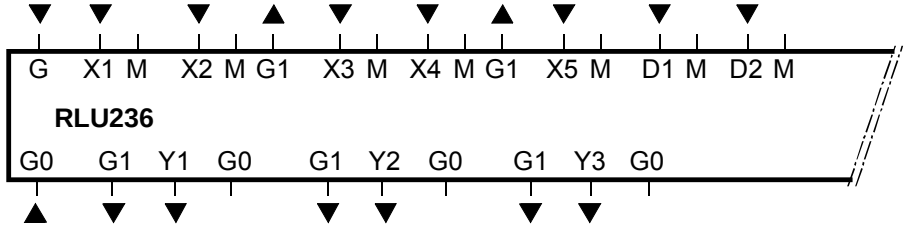

G X1 M X2 M G1 X3 M X4 M G1 D1 M Q11 Q23
RLU202
G0 N1 Q12 Q14 N2 Q24

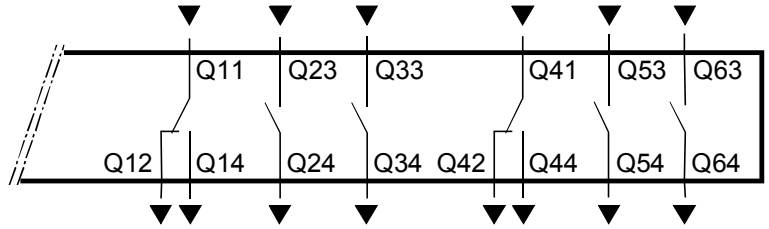

G X1 M X2 M G1 X3 M D1 M
RLU210
G0 G1 Y1 G0


G X1 M X2 M G1 X3 M X4 M G1 D1 M
RLU220
G0 G1 Y1 G0 G1 Y2 G0

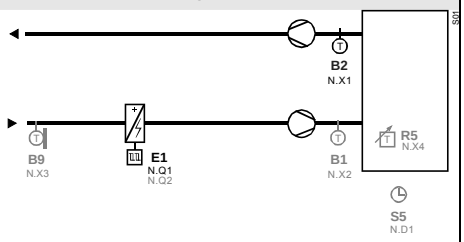

G X1 M X2 M G1 X3 M X4 M G1 D1 M Q11 Q23
RLU222
G0 G1 Y1 G0 G1 Y2 G0 N1 Q12 Q14 N2 Q24


G X1 M X2 M G1 X3 M X4 M G1 X5 M D1 M D2 M Q11 Q33
RLU232
G0 G1 Y1 G0 G1 Y2 G0 G1 Y3 G0 Q12 Q14 Q34

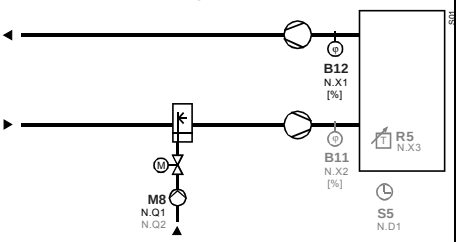

G X1 M X2 M G1 X3 M X4 M G1 X5 M D1 M D2 M
RLU236
G0 G1 Y1 G0 G1 Y2 G0 G1 Y3 G0


Q11 Q23 Q33 Q41 Q53 Q63
Q12 Q14 Q24 Q34 Q42 Q44 Q54 Q64

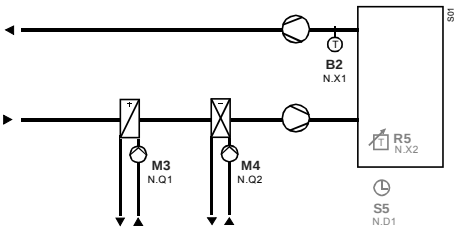
ADA006 LU0 HQ – RLU202 – A01



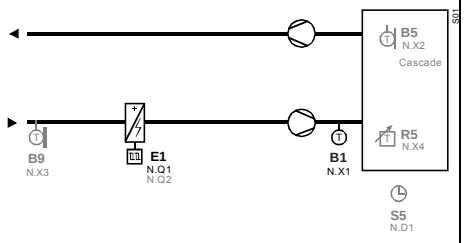
ADKA02 LU0 HQ – RLU202 – U01



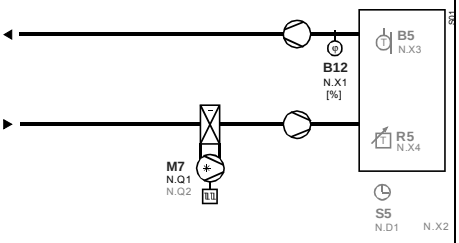
ADC019 LU0 HQ – RLU202 – U08



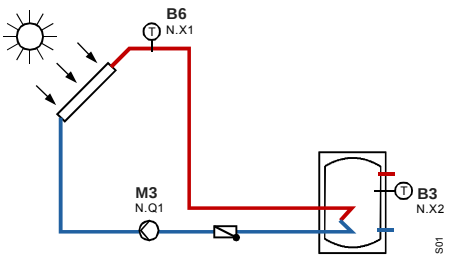
ADA012 LU0 HQ – RLU202 – A02



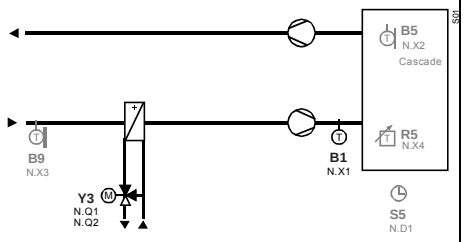
ADI003 LU0 HQ – RLU202 – U02



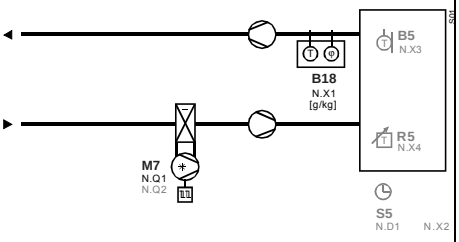
SA0001 LU0 HQ – RLU202 – U09



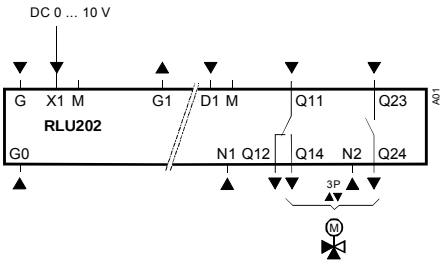
ADA014 LU0 HQ – RLU202 – A03



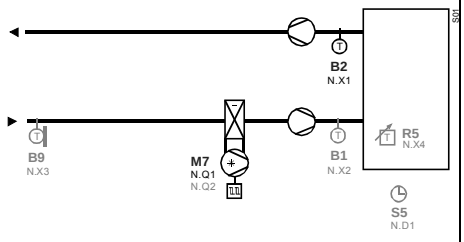
ADI005 LU0 HQ – RLU202 – U03



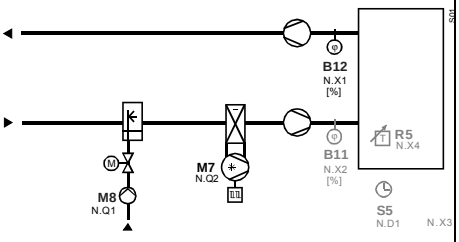
ZZZ001 LU0 HQ – RLU202 – U10



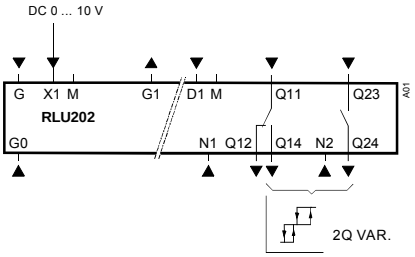
ADB003 LU0 HQ – RLU202 – A04



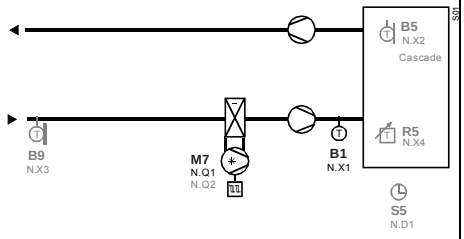
ADZA01 LU0 HQ – RLU202 – U04



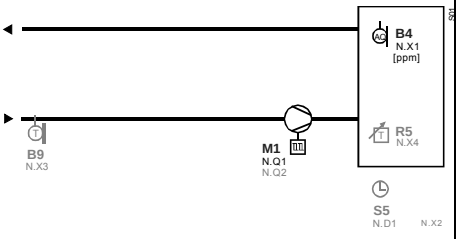
ZZZ002 LU0 HQ – RLU202 – U11



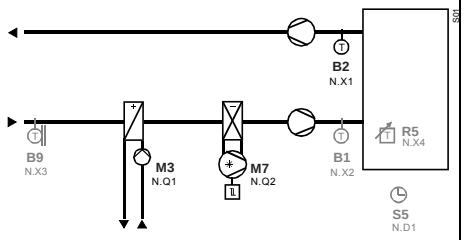
ADB007 LU0 HQ – RLU202 – A05



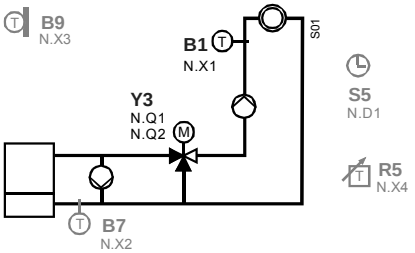
AZZ001 LU0 HQ – RLU202 – U05



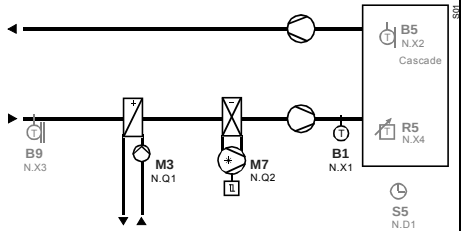
AEC001 LU0 HQ – RLU202 – A06



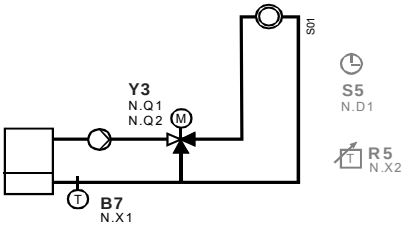
HZC001 LU0 HQ – RLU202 – U06



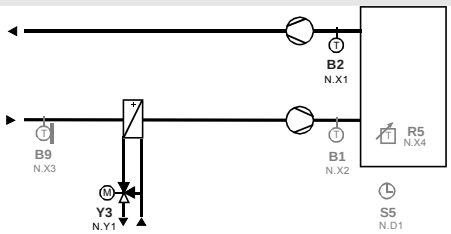
AEC002 LU0 HQ – RLU202 – A07



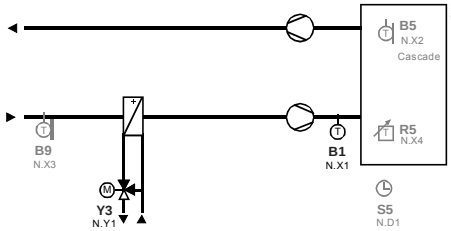
HZC002 LU0 HQ – RLU202 – U07



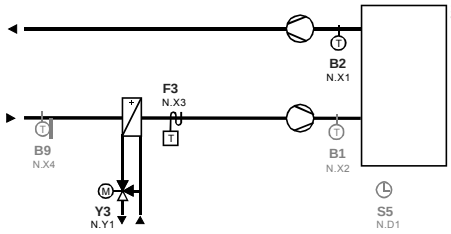
ADA001 LU2 HQ – RLU220 – A01



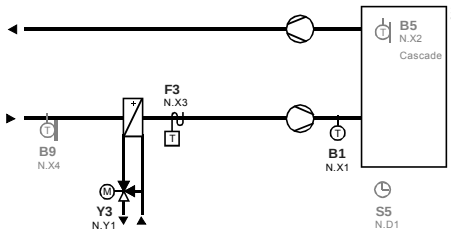
ADA008 LU2 HQ – RLU220 – A02



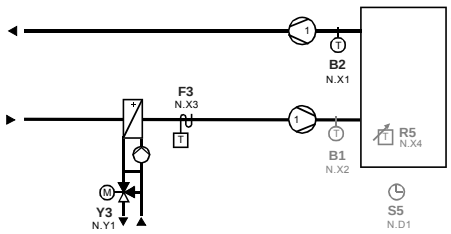
ADA003 LU2 HQ – RLU220 – A03



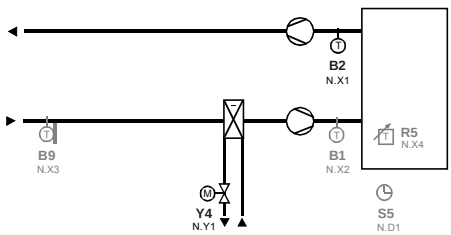
ADA010 LU2 HQ – RLU220 – A04



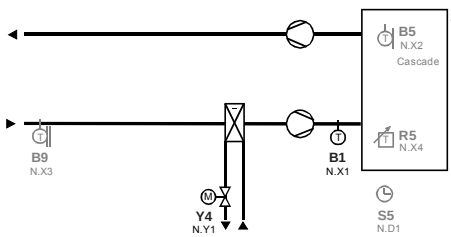
ADA017 LU2 HQ – RLU220 – A05



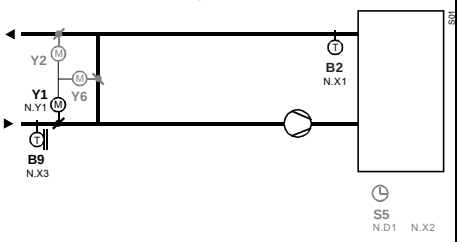
ADB001 LU2 HQ – RLU220 – A06



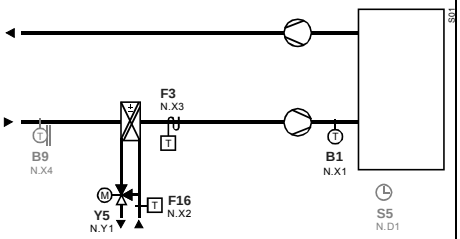
ADB005 LU2 HQ – RLU220 – A07



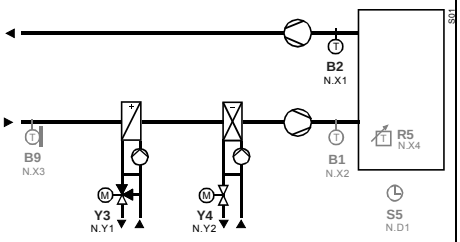
ACAD01 LU2 HQ – RLU220 – A08



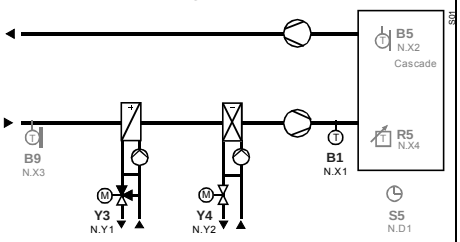
ABC001 LU2 HQ – RLU220 – A09



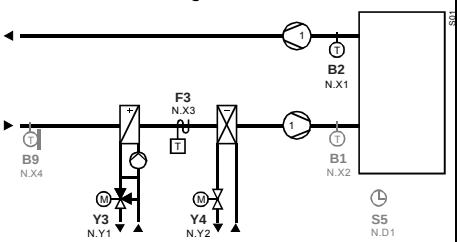
ADC021 LU2 HQ – RLU220 – A10



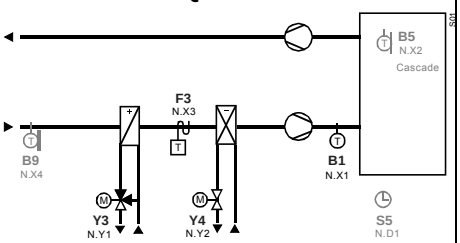
ADC023 LU2 HQ – RLU220 – A11



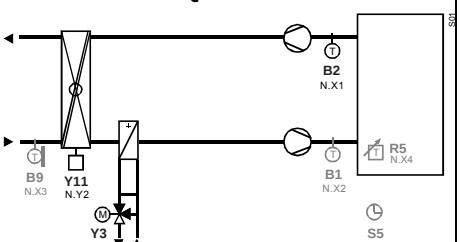
ADC022 LU2 HQ – RLU220 – A12



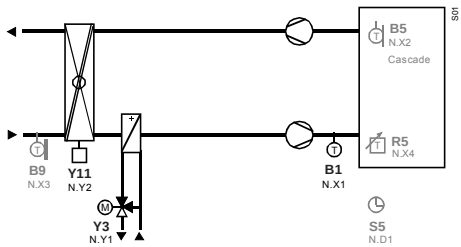
ADC024 LU2 HQ – RLU220 – A13



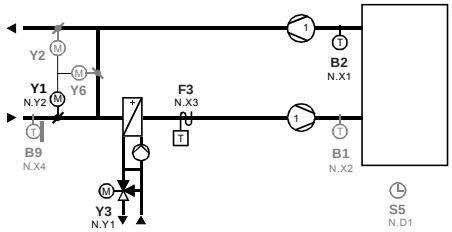
AEAF03 LU2 HQ – RLU220 – A14



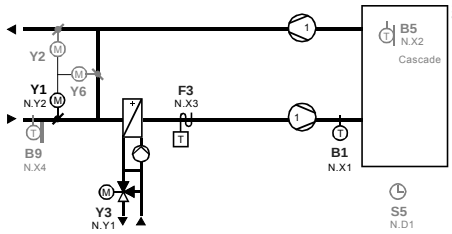
AEAF04 LU2 HQ – RLU220 – A15



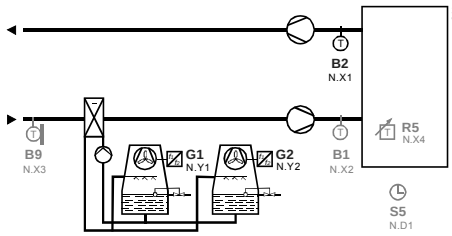
AEAD03 LU2 HQ – RLU220 – A16



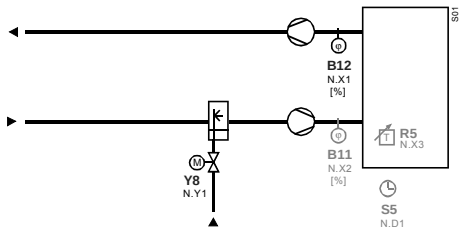
AEAD04 LU2 HQ – RLU220 – A17



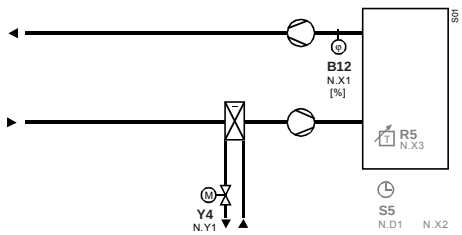
ADB012 LU2 HQ – RLU220 – A18



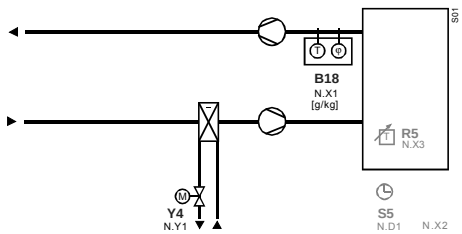
ADKA01 LU2 HQ – RLU220 – U01



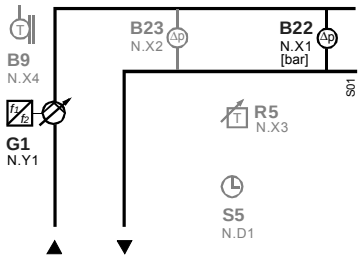
ADI001 LU2 HQ – RLU220 – U02



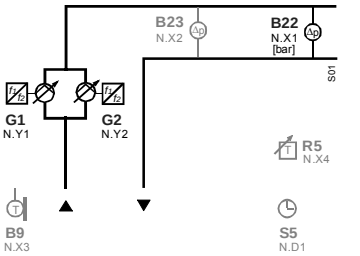
ADI002 LU2 HQ – RLU220 – U03



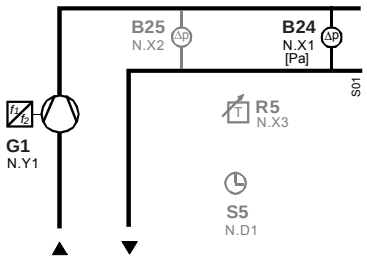
PB0001 LU2 HQ – RLU220 – U04



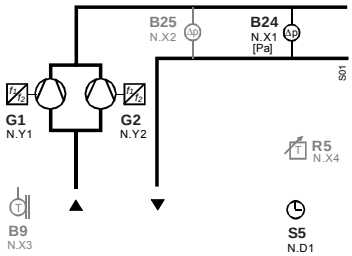
PB0004 LU2 HQ – RLU220 – U05



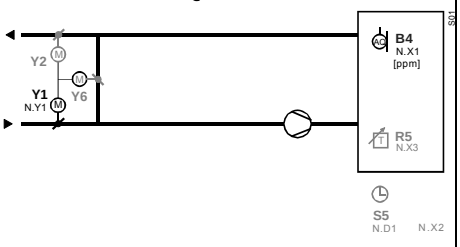
AZL001 LU2 HQ – RLU220 – U06



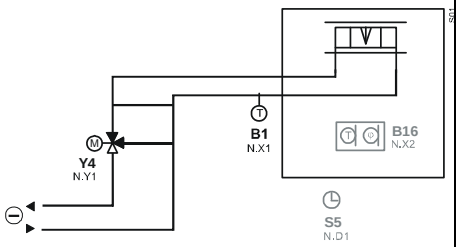
AZL004 LU2 HQ – RLU220 – U07



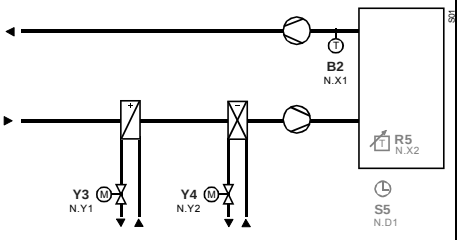
AAZD01 LU2 HQ – RLU220 – U08



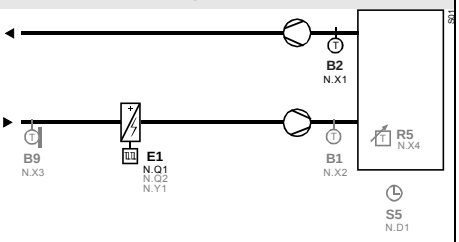
CZC002 LU2 HQ – RLU220 – U09



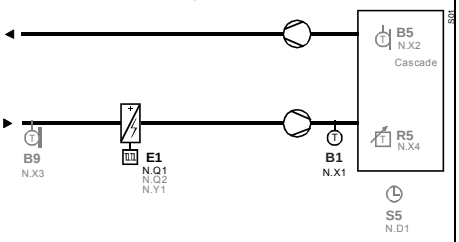
ADC025 LU2 HQ – RLU220 – U10



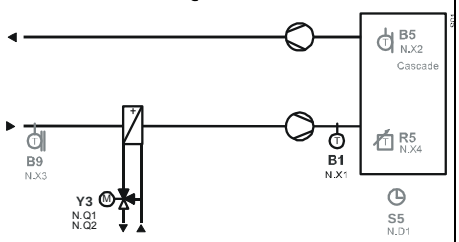
ADA006 LU2 HQ – RLU222 – A01



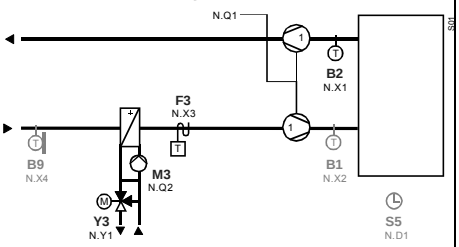
ADA012 LU2 HQ – RLU222 – A02



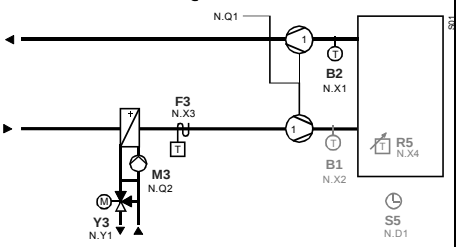
ADA014 LU2 HQ – RLU222 – A03



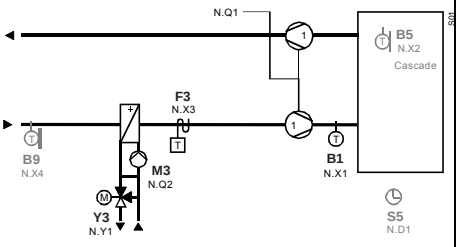
ADA004 LU2 HQ – RLU222 – A04



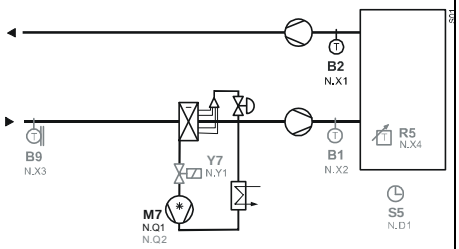
ADA005 LU2 HQ – RLU222 – A05



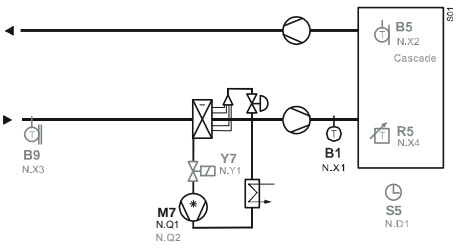
ADA011 LU2 HQ – RLU222 – A06



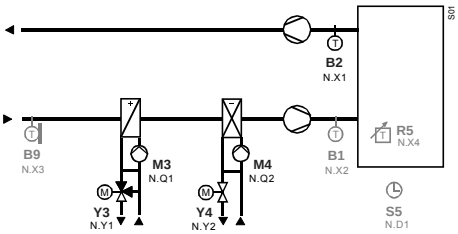
ADB003 LU2 HQ – RLU222 – A07



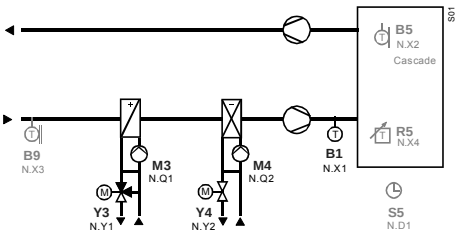
ADB007 LU2 HQ – RLU222 – A08



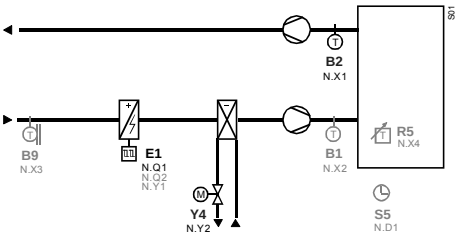
ADC002 LU2 HQ – RLU222 – A09



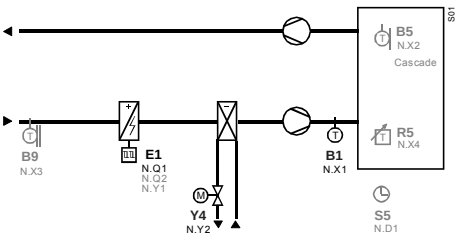
ADC010 LU2 HQ – RLU222 – A10



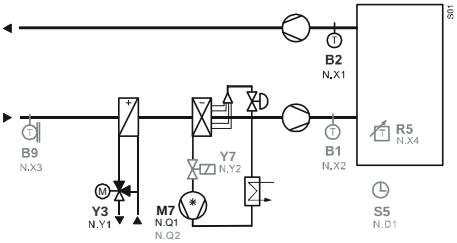
ADC004 LU2 HQ – RLU222 – A11



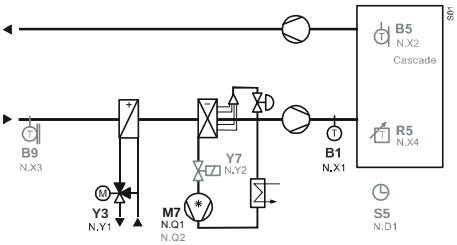
ADC012 LU2 HQ – RLU222 – A12



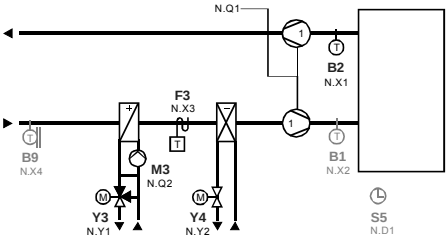
ADC003 LU2 HQ – RLU222 – A13



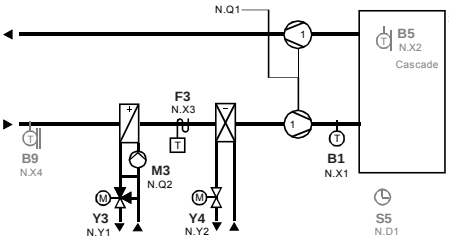
ADC011 LU2 HQ – RLU222 – A14



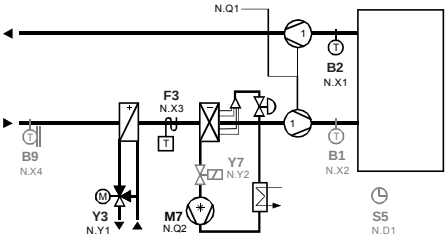
ADC006 LU2 HQ – RLU222 – A15



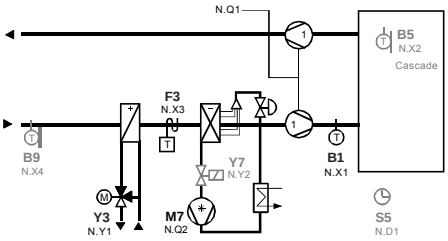
ADC014 LU2 HQ – RLU222 – A16



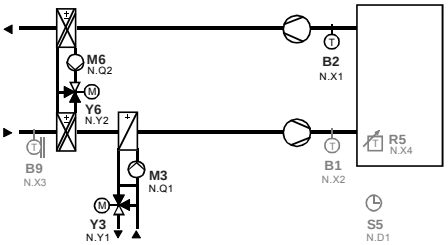
ADC007 LU2 HQ – RLU222 – A17



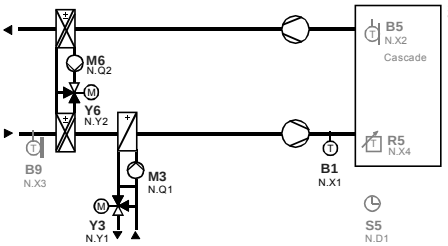
ADC016 LU2 HQ – RLU222 – A18



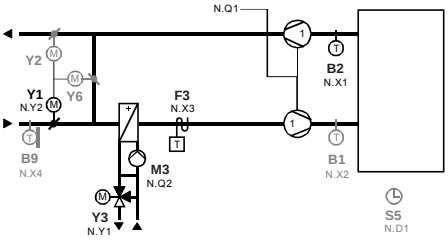
AEAG01 LU2 HQ – RLU222 – A19



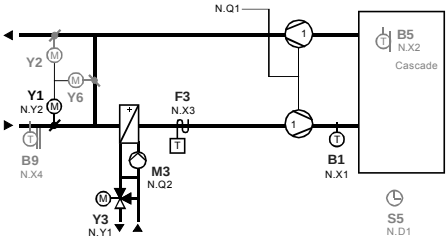
AEAG02 LU2 HQ – RLU222 – A20



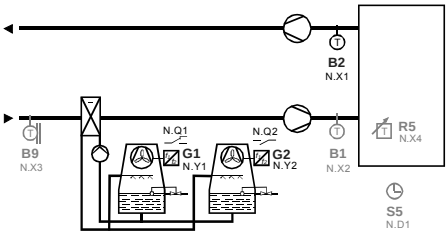
AEAD01 LU2 HQ – RLU222 – A21



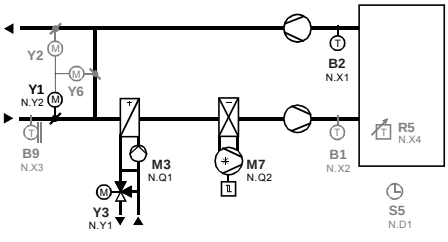
AEAD02 LU2 HQ – RLU222 – A22



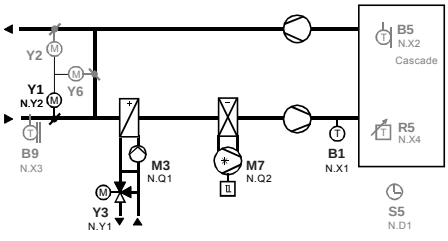
ADB009 LU2 HQ – RLU222 – A23



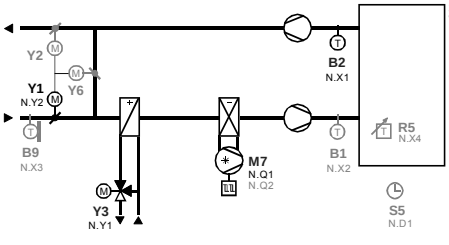
AECD01 LU2 HQ – RLU222 – A24



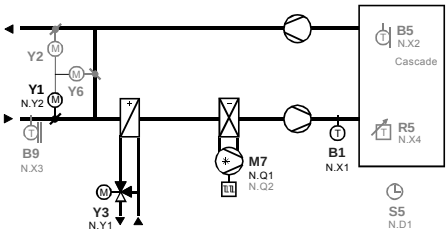
AECD04 LU2 HQ – RLU222 – A25



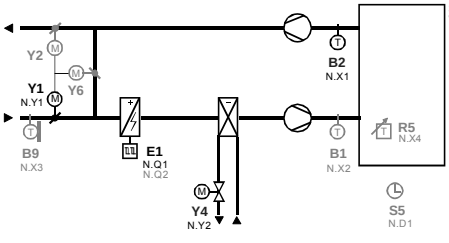
AECD02 LU2 HQ – RLU222 – A26



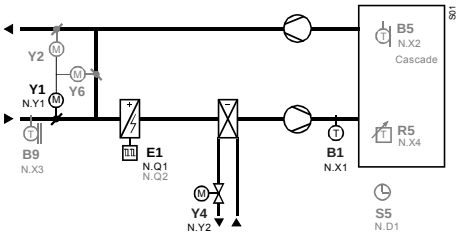
AECD05 LU2 HQ – RLU222 – A27



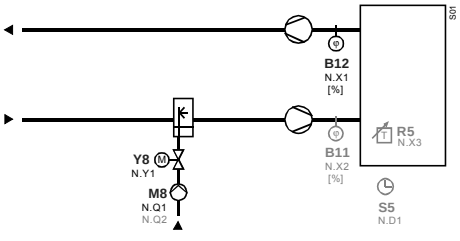
AECD03 LU2 HQ – RLU222 – A28



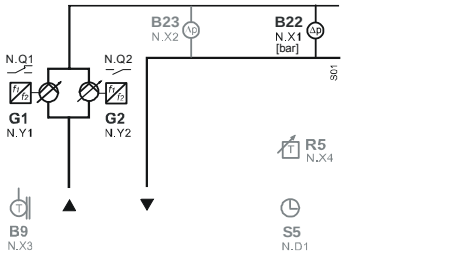
AECD06 LU2 HQ – RLU222 – A29



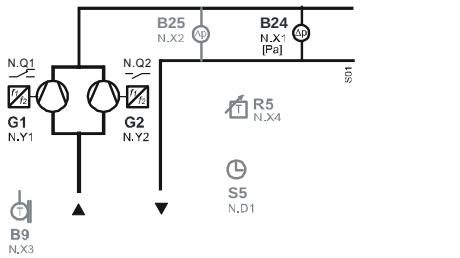
ADKA02 LU2 HQ – RLU222 – U01



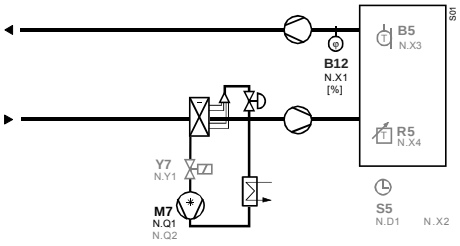
PB0002 LU2 HQ – RLU222 – U02



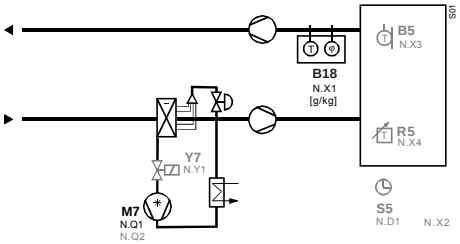
AZL002 LU2 HQ – RLU222 – U03



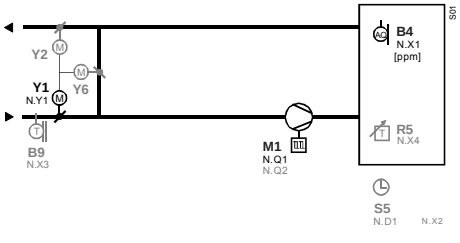
ADI003 LU2 HQ – RLU222 – U04



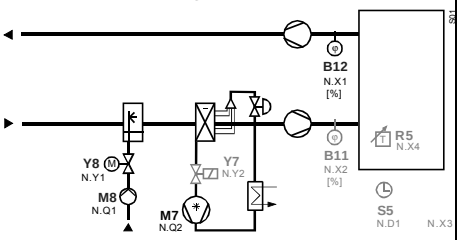
ADI005 LU2 HQ – RLU222 – U05



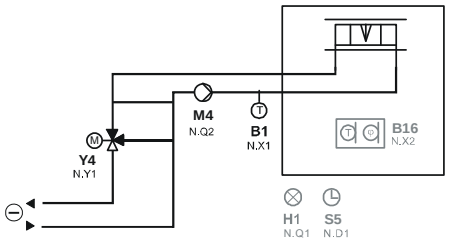
AAZD02 LU2 HQ – RLU222 – U06



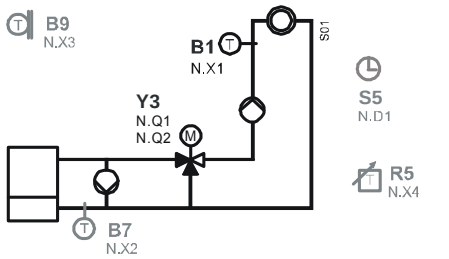
ADZA01 LU2 HQ – RLU222 – U07



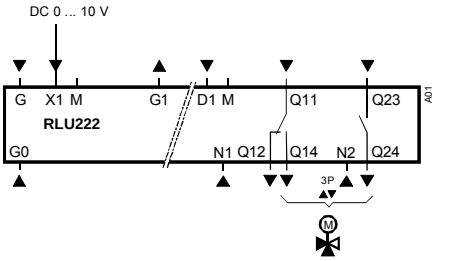
CZC001 LU2 HQ – RLU222 – U08



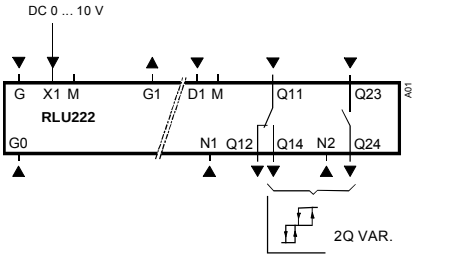
HZC001 LU2 HQ – RLU222 – U09



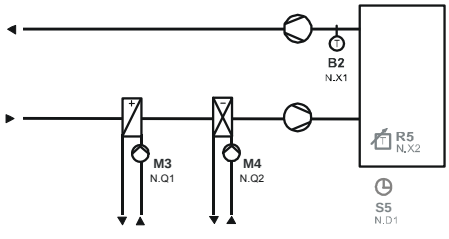
ZZZ001 LU2 HQ – RLU222 – U10



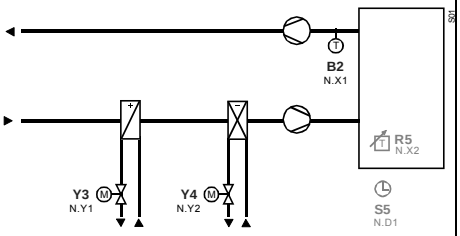
ZZZ002 LU2 HQ – RLU222 – U11



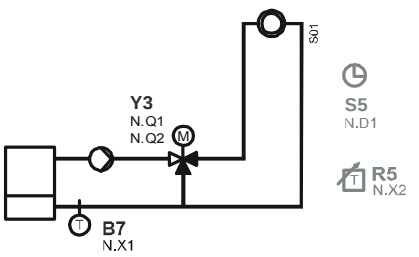
ADC019 LU2 HQ – RLU222 – U12



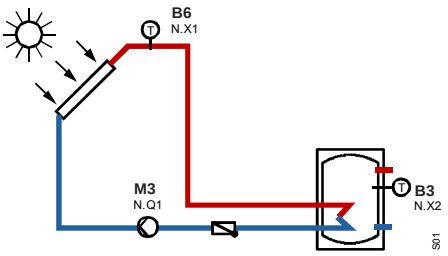
ADC020 LU2 HQ – RLU222 – U13



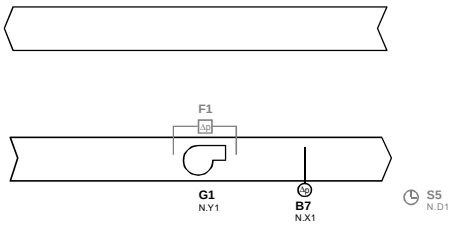
HZC002 LU2 HQ – RLU222 – U14



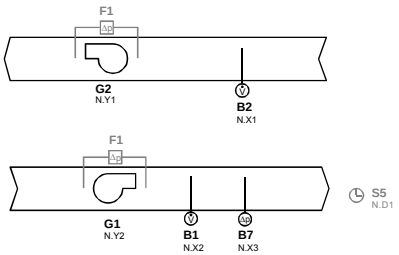
SA0001 LU2 HQ – RLU222 – U15



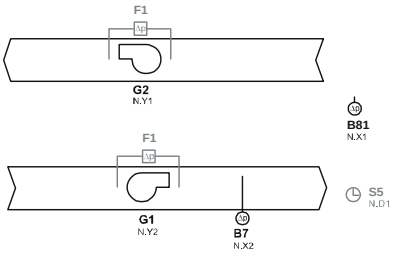
ABL001 LU2 HQ – RLU222 – U16



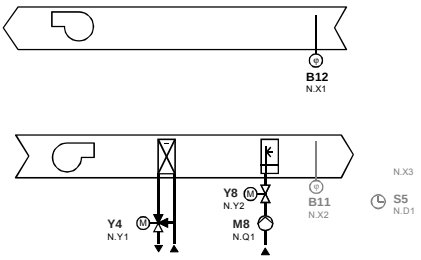
ADZ001 LU2 HQ – RLU222 – U17



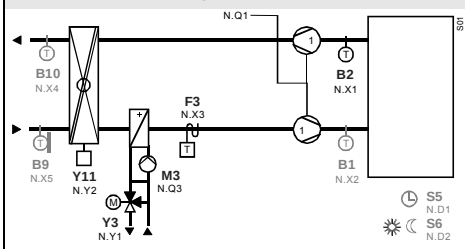
ADL001 LU2 HQ – RLU222 – U18



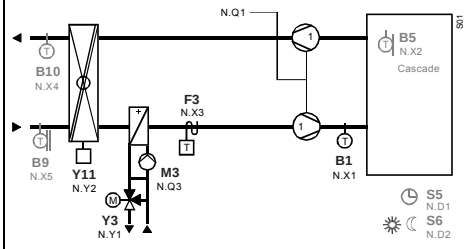
ADZA02 LU2 HQ – RLU222 – U19



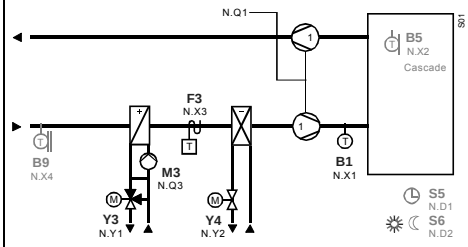
AEAF01 LU3 HQ – RLU232 – A01



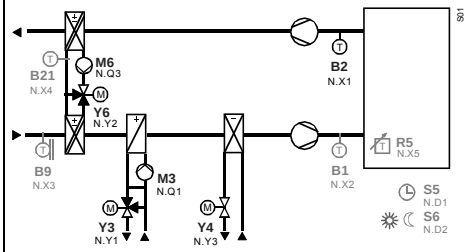
AEAF02 LU3 HQ – RLU232 – A02



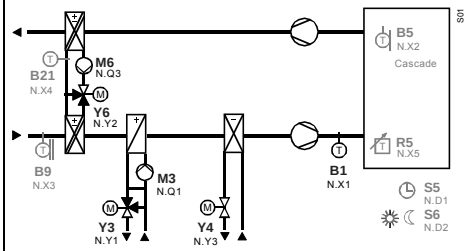
ADC015 LU3 HQ – RLU232 – A03



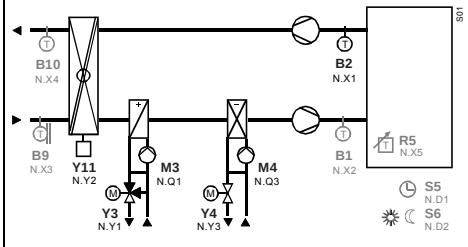
AECG01 LU3 HQ – RLU232 – A04



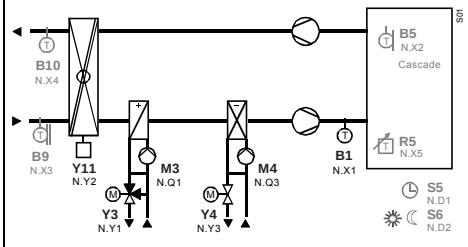
AECG02 LU3 HQ – RLU232 – A05



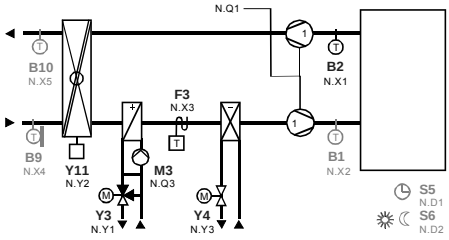
AECF01 LU3 HQ – RLU232 – A06



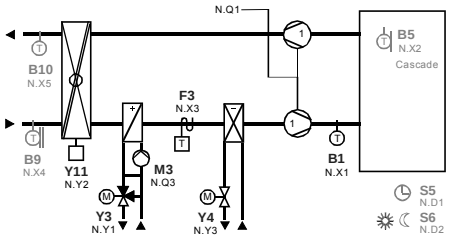
AECF03 LU3 HQ – RLU232 – A07



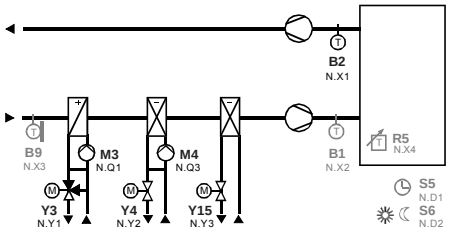
AECF02 LU3 HQ – RLU232 – A08



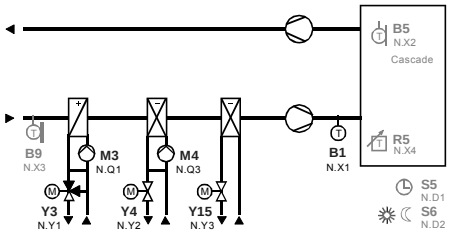
AECF04 LU3 HQ – RLU232 – A09



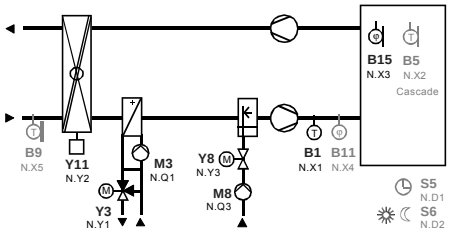
ADC009 LU3 HQ – RLU232 – A10



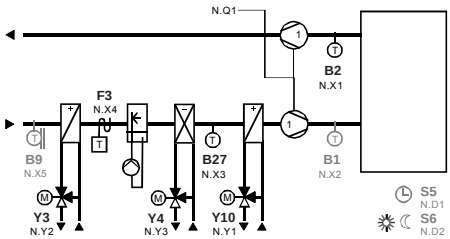
ADC018 LU3 HQ – RLU232 – A11



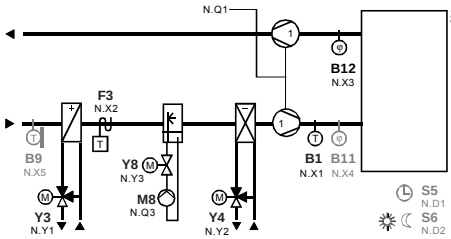
AEDK01 LU3 HQ – RLU232 – A12



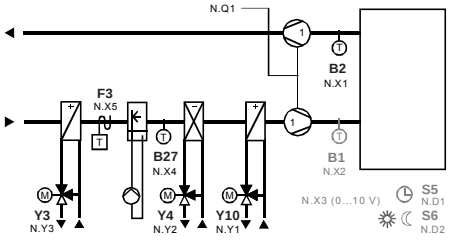
ADE001 LU3 HQ – RLU232 – A13



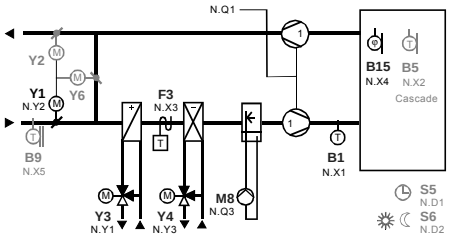
ADFA01 LU3 HQ – RLU232 – A14



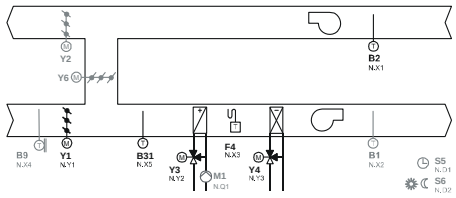
ADFA02 LU3 HQ – RLU232 – A15



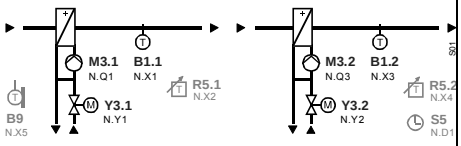
AEFH01 LU3 HQ – RLU232 – A16



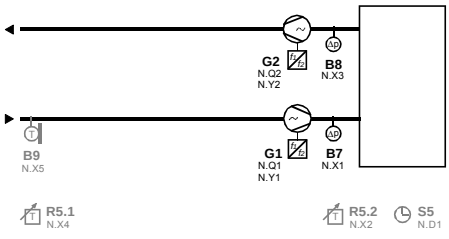
AECD01 LU3 HQ – RLU232 – A17



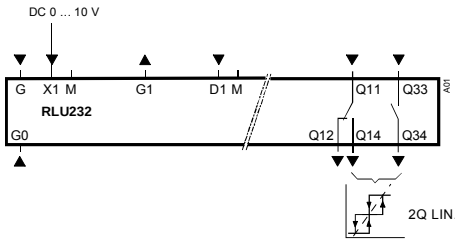
ABA001 LU3 HQ – RLU232 – U01



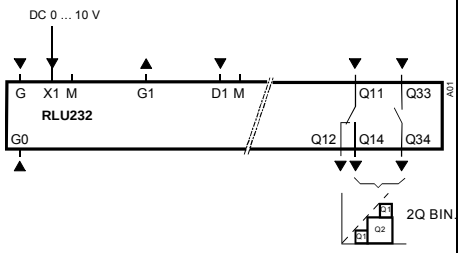
ADL001 LU3 HQ – RLU232 – U02



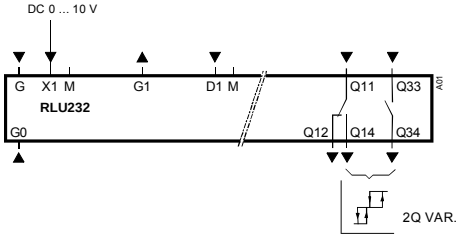
ADL003 LU3 HQ – RLU232 – U03



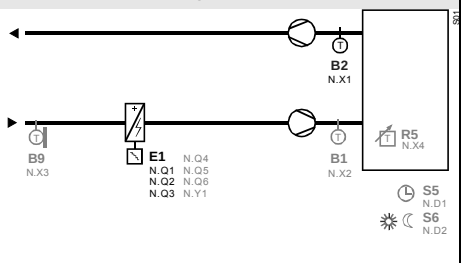
ZZZ004 LU3 HQ – RLU232 – U04



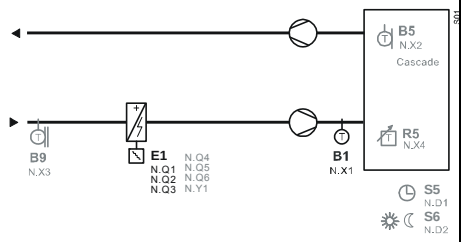
ZZZ005 LU3 HQ – RLU232 – U05



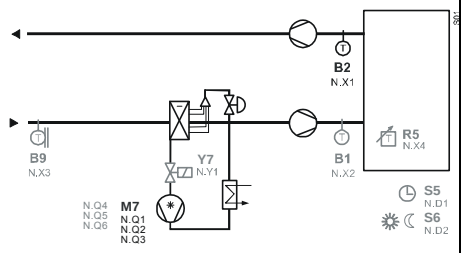
ADA007 LU3 HQ – RLU236 – A01



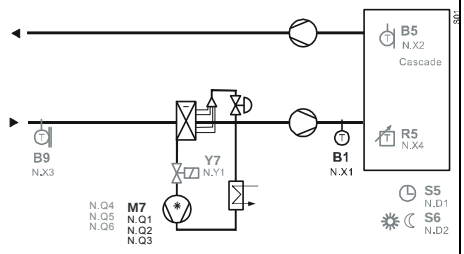
ADA013 LU3 HQ – RLU236 – A02



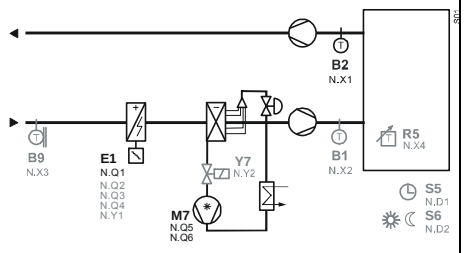
ADB004 LU3 HQ – RLU236 – A03



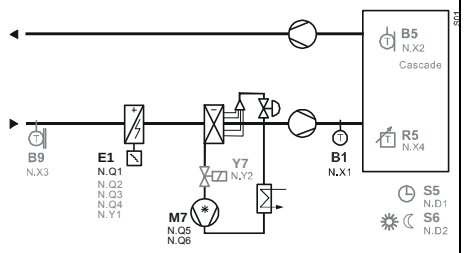
ADB008 LU3 HQ – RLU236 – A04



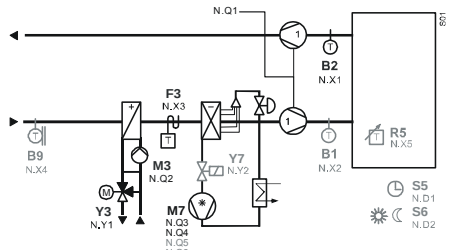
ADC005 LU3 HQ – RLU236 – A05



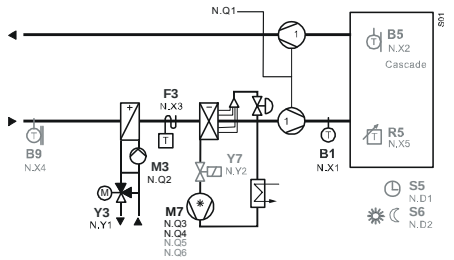
ADC013 LU3 HQ – RLU236 – A06



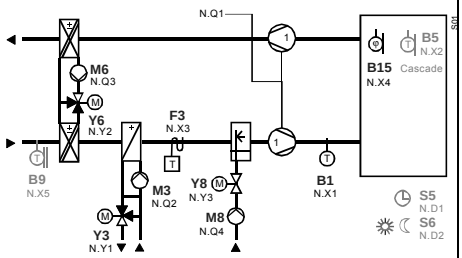
ADC008 LU3 HQ – RLU236 – A07



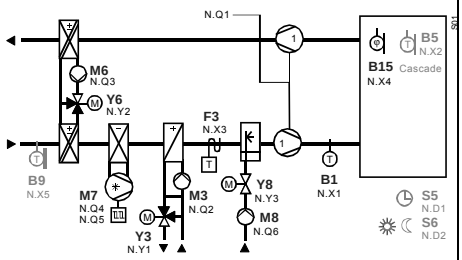
ADC017 LU3 HQ – RLU236 – A08



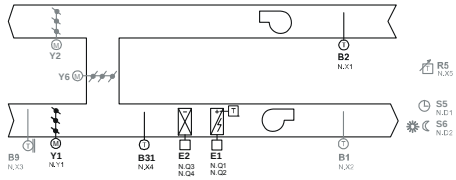
AEDL01 LU3 HQ – RLU236 – A09



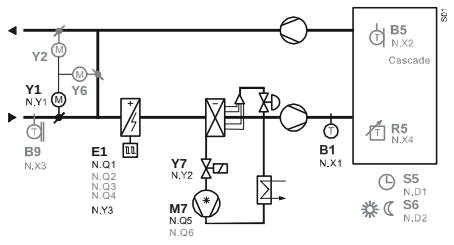
AEFL01 LU3 HQ – RLU236 – A10



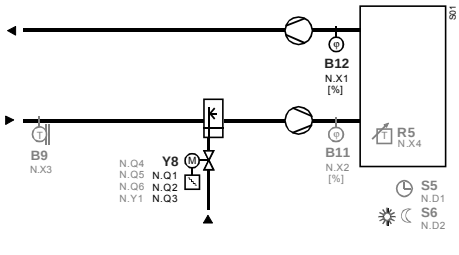
AECD02 LU3 HQ – RLU236 – A11



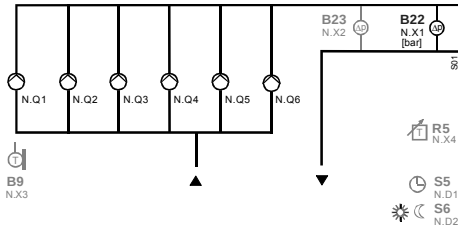
AECD03 LU3 HQ – RLU236 – A14



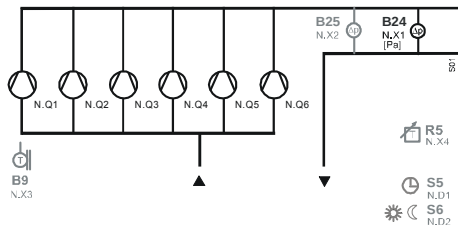
ADKA03 LU3 HQ – RLU236 – U01



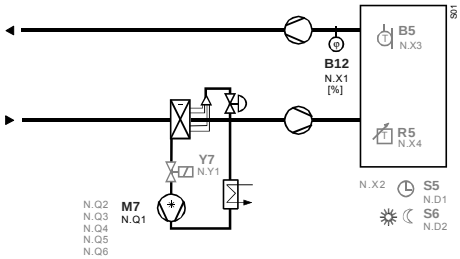
PB0003 LU3 HQ – RLU236 – U02



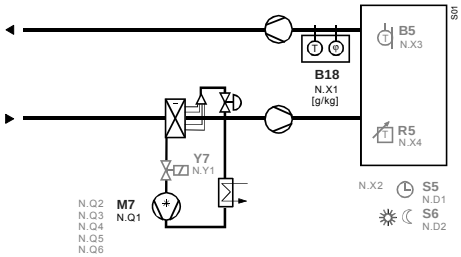
AZL003 LU3 HQ – RLU236 – U03



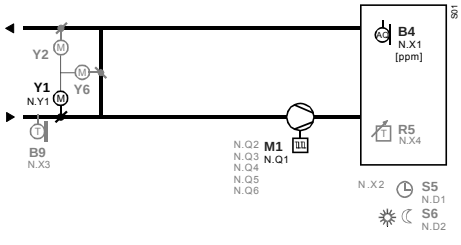
ADI004 LU3 HQ – RLU236 – U04



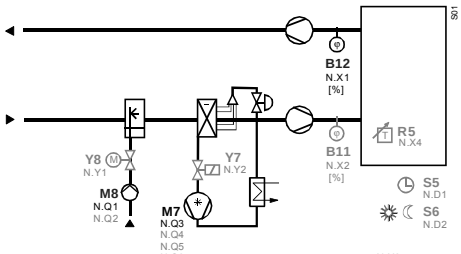
ADI006 LU3 HQ – RLU236 – U05



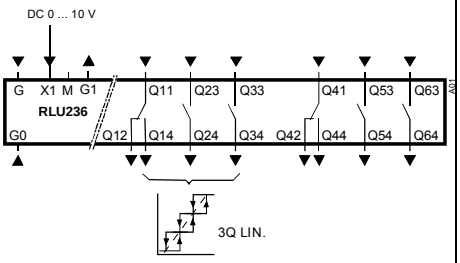
AAZD03 LU3 HQ – RLU236 – U06



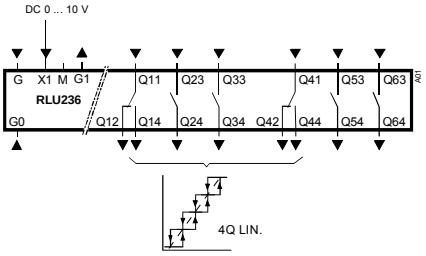
ADZA02 LU3 HQ – RLU236 – U07



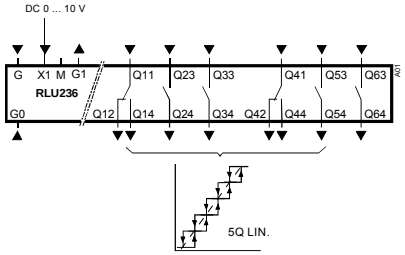
ZZZ006 LU3 HQ – RLU236 – U08



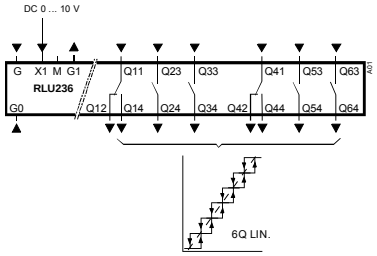
ZZZ007 LU3 HQ – RLU236 – U09



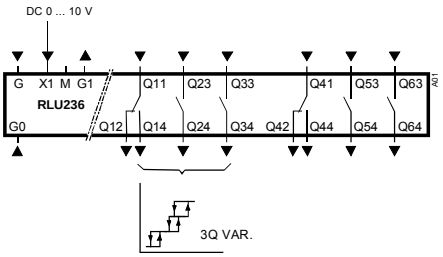
ZZZ008 LU3 HQ – RLU236 – U10



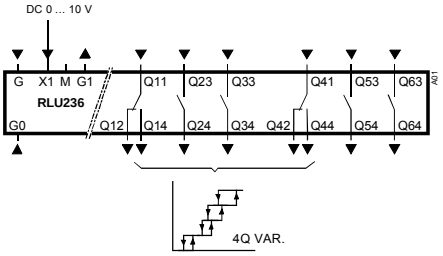
ZZZ009 LU3 HQ – RLU236 – U11



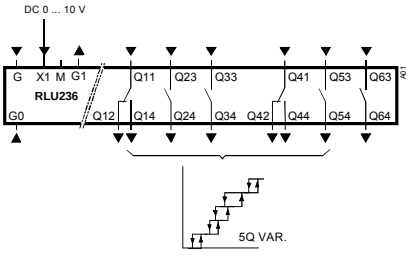
ZZZ010 LU3 HQ – RLU236 – U12



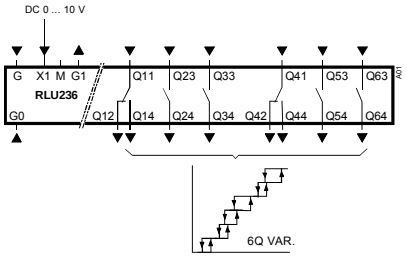
ZZZ011 LU3 HQ – RLU236 – U13



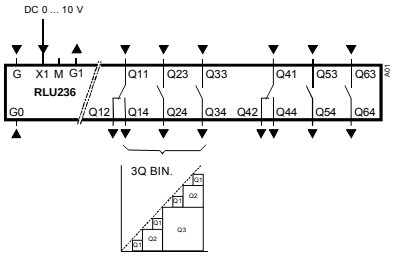
ZZZ012 LU3 HQ – RLU236 – U14



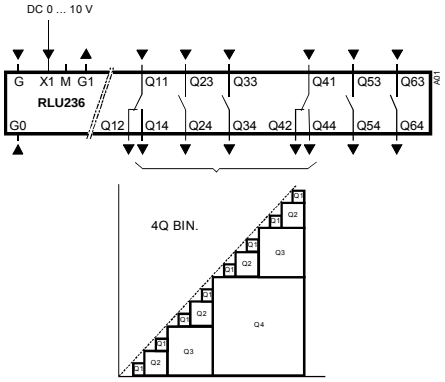
ZZZ013 LU3 HQ – RLU236 – U15



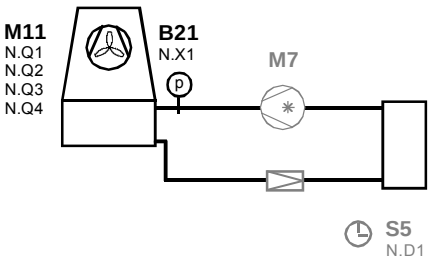
ZZZ014 LU3 HQ – RLU236 – U16



ZZZ015 LU3 HQ – RLU236 – U17



CZZ001 LU3 HQ – RLU236 – U18



（注記）記載の内容はお断りなく変更する場合が有ります。

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